



**(43) International Publication Date**  
**4 August 2005 (04.08.2005)**

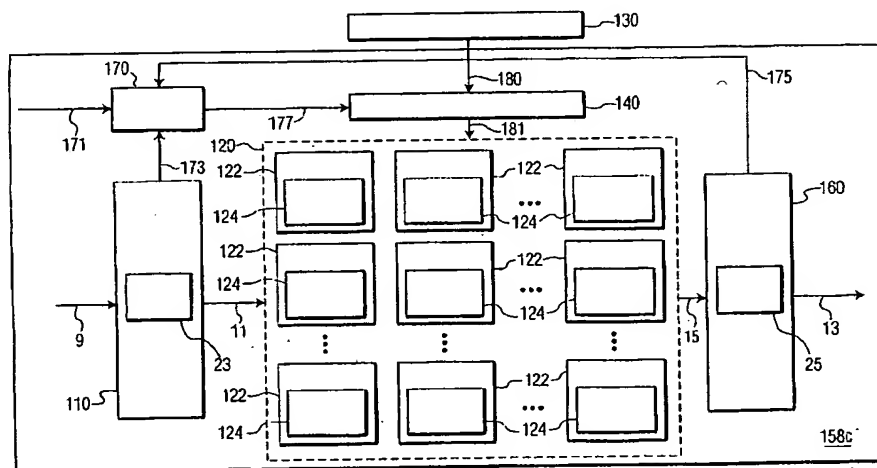
**PCT**

**(10) International Publication Number**  
**WO 2005/071850 A2**

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| <p>(51) <b>International Patent Classification</b><sup>7</sup>: <b>H04B 1/38</b></p> <p>(21) <b>International Application Number</b>:<br/>PCT/IB2005/050258</p> <p>(22) <b>International Filing Date</b>: 21 January 2005 (21.01.2005)</p> <p>(25) <b>Filing Language</b>: English</p> <p>(26) <b>Publication Language</b>: English</p> <p>(30) <b>Priority Data</b>:<br/>60/538,275      22 January 2004 (22.01.2004)      US</p> <p>(71) <b>Applicant (for all designated States except US)</b>: <b>KONINKLIJKE PHILIPS ELECTRONICS, N.V.</b> [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).</p> <p>(71) <b>Applicant (for AE only)</b>: <b>U.S. PHILIPS CORPORATION</b> [US/US]; 1251 Avenue of the Americas, New York, NY 10020 (US).</p> <p>(72) <b>Inventors; and</b></p> <p>(75) <b>Inventors/Applicants (for US only)</b>: <b>VAIDYANATHAN, Krishnamurthy</b> [NL/NL]; Groenewoudseweg 1, NL-5621</p> | <p>BA Eindhoven (NL). <b>WITTIG, Karl, R.</b> [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). <b>BURNS, Geoffrey, F.</b> [NL/NL]; Groenewoudseweg 1; NL-5621 BA Eindhoven (NL).</p> <p>(74) <b>Common Representative</b>: <b>KONINKLIJKE PHILIPS ELECTRONICS, N.V.</b>; c/o HALAJIAN, Digran, P.O. Box 3001, Briarcliff Manor, NY 10510-8001 (US).</p> <p>(81) <b>Designated States (unless otherwise indicated, for every kind of national protection available)</b>: AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.</p> <p>(84) <b>Designated States (unless otherwise indicated, for every kind of regional protection available)</b>: ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,</p> |
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**(54) Title:** A DYNAMICALLY RECONFIGURABLE SIGNAL PROCESSING APPARATUS AND METHOD FOR USE IN A HIGH SPEED DIGITAL COMMUNICATION SYSTEM



**(57) Abstract:** A dynamically reconfigurable signal processing apparatus (158c) is disclosed. The signal processing apparatus (158c) includes at least one system controller (170) for detecting a change of state in a high speed digital communication system (10). Responsive to the change of state, in one embodiment, signal processing function code is downloaded, in real-time or in near real-time, from an external memory (130) to be processed in an array-type processor (120). In another embodiment, signal processing function code for a plurality of signal processing functions are pre-stored in the arraytype processor (120) and are switch selectable in response to a change of system state thereby obviating the need to download signal processing function code.

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**Declarations under Rule 4.17:**

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM,

AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations

**Published:**

- without international search report and to be republished upon receipt of that report

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